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*Kihun Kim; Joonyong Kim; Hang S. Choi; Kyu-Yeul Lee; Woojae Seong;*  
Oceans '02 MTS/IEEE , Volume: 1 , Oct. 29-31, 2002  
Page(s): 186 -190

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**2 Tracking down software bugs using automatic anomaly detection**

*Hangal, S.; Lam, M.S.;*  
Software Engineering, 2002. ICSE 2002. Proceedings of the 24rd International Conference on , 19-25 May 2002  
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**3 Performance analysis and validation of the picoJava processor**

*Hangal, S.; O'Connor, M.;*  
Micro, IEEE , Volume: 19 Issue: 3 , May-June 1999  
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**4 Decimation filter compiler for oversampling A/D applications**

*Hang, S.-S.; Jain, R.;*  
Acoustics, Speech, and Signal Processing, 1992. ICASSP-92., 1992 IEEE International Conference on , Volume: 5 , 23-26 March 1992  
Page(s): 537 -540 vol.5

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**1 A 90 V breakdown high temperature SOI lateral power nMOSFET**

*Tsang, J.C.; McKitterick, J.B.; Zolnier, J.A.; O'Connor, J.M.*

High Temperature Electronics Conference, 1998. HITEC. 1998 Fourth International , 14-18 June 1998  
 Page(s): 215 -218

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) **IEEE CNF**

**2 225 C high temperature silicon-on-insulator (SOI) ASICs for harsh environments**

*O'Connor, J.M.; Tsang, J.C.; McKitterick, J.B.*

Integrated Power Packaging, 1998. IWIPPP. Proceedings., IEEE International Workshop on , 17-19 Sept. 1998  
 Page(s): 2 -5

[\[Abstract\]](#) [\[PDF Full-Text \(296 KB\)\]](#) **IEEE CNF**

**3 picoJava-I: the Java virtual machine in hardware**

*O'Connor, J.M.; Tremblay, M.*

Micro, IEEE , Volume: 17 Issue: 2 , March-April 1997  
 Page(s): 45 -53

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**4 VIS speeds new media processing**

*Tremblay, M.; O'Connor, J.M.; Narayanan, V.; Liang He;*

Micro, IEEE , Volume: 16 Issue: 4 , Aug. 1996  
 Page(s): 10 -20

[\[Abstract\]](#) [\[PDF Full-Text \(1120 KB\)\]](#) **IEEE JNL**

**5 UltraSparc I: a four-issue processor supporting multimedia**

*Tremblay, M.; O'Connor, J.M.*

Micro, IEEE , Volume: 16 Issue: 2 , April 1996  
 Page(s): 42 -50

[\[Abstract\]](#) [\[PDF Full-Text \(1164 KB\)\]](#) **IEEE JNL**

**6 Incas: a cycle accurate model of UltraSPARC**

*Maturana, G.; Ball, J.L.; Gee, J.; Iyer, A.; O'Connor, J.M.*

Computer Design: VLSI in Computers and Processors, 1995. ICCD '95. Proceedings., 1995 IEEE International Conference on , 2-4 Oct. 1995  
 Page(s): 130 -135

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**7 A 1.45-W/mm, 30-GHz InP-channel power HEMT**

*Aina, O.; Burgess, M.; Mattingly, M.; Meerschaert, A.; O'Connor, J.M.; Tong, M.; Ketterson, A.; Adesida, I.;*  
Electron Device Letters, IEEE , Volume: 13 Issue: 5 , May 1992  
Page(s): 300 -302

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**8 Novel InP-channel HEMTs grown by organometallic vapor phase epitaxy**

*Aina, L.; Mattingly, M.; Burgess, M.; Hempfling, E.; Meerschaert, A.; O'Connor, J.M.;*  
Indium Phosphide and Related Materials, 1991., Third International Conference. , 8-11 April 1991  
Page(s): 381 -384

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**9 Growth of high purity InP and fabrication of high sensitivity InP/GaInAs heterojunction phototransistors on silicon by OMVPE**

*Aina, L.; Mattingly, M.; Burgess, M.; O'Connor, J.M.; Shastry, S.K.; Hill, D.S.; Salerno, J.P.; Davis, A.; Lorenzo, J.P.;*  
Indium Phosphide and Related Materials, 1991., Third International Conference. , 8-11 April 1991  
Page(s): 118 -121

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**10 0.33- $\mu$ m gate-length millimeter-wave InP-channel HEMTs with high  $f_1$  and  $f_{max}$**

*Aina, L.; Burgess, M.; Mattingly, M.; O'Connor, J.M.; Meerschaert, A.; Tong, M.; Ketterson, A.; Adesida, I.;*  
Electron Device Letters, IEEE , Volume: 12 Issue: 9 , Sept. 1991  
Page(s): 483 -485

[\[Abstract\]](#) [\[PDF Full-Text \(180 KB\)\]](#) **IEEE JNL**

---

**11 High voltage DMOS power FETs on thin SOI substrates**

*O'Connor, J.M.; Luciani, V.K.; Caviglia, A.L.;*  
SOS/SOI Technology Conference, 1990., 1990 IEEE , 2-4 Oct. 1990  
Page(s): 167 -168

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**1 Cycle-accurate energy estimation in system level descriptions of embedded systems**

*Belen Abril Garcia, A.; Gobert, J.; Dombek, T.; Mehrez, H.; Petrot, F.;*  
Electronics, Circuits and Systems, 2002. 9th International Conference on, Volume: 2, 15-18 Sept. 2002  
Page(s): 549 -552 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(522 KB\)\] IEEE CNF](#)

**2 Coarse reconfigurable multimedia unit extension**

*Wong, S.; Cotofana, S.; Vassiliadis, S.;*  
Parallel and Distributed Processing, 2001. Proceedings. Ninth Euromicro Workshop on, 7-9 Feb. 2001  
Page(s): 235 -242

[\[Abstract\]](#) [\[PDF Full-Text \(736 KB\)\] IEEE CNF](#)

**3 Hybridizing and coalescing load value predictors**

*Burtscher, M.; Zorn, B.G.;*  
Computer Design, 2000. Proceedings. 2000 International Conference on, 17-20 Sept. 2000  
Page(s): 81 -92

[\[Abstract\]](#) [\[PDF Full-Text \(1116 KB\)\] IEEE CNF](#)

**4 Cycle-accurate simulation of energy consumption in embedded systems**

*Simunic, T.; Benini, L.; De Micheli, G.;*  
Design Automation Conference, 1999. Proceedings. 36th, 21-25 June 1999  
Page(s): 867 -872

[\[Abstract\]](#) [\[PDF Full-Text \(600 KB\)\] IEEE CNF](#)

**5 Exploring last n value prediction**

*Burtscher, M.; Zorn, B.G.;*  
Parallel Architectures and Compilation Techniques, 1999. Proceedings. 1999 International Conference on, 12-16 Oct. 1999  
Page(s): 66 -76

[\[Abstract\]](#) [\[PDF Full-Text \(120 KB\)\] IEEE CNF](#)

**6 An integrated functional performance simulator**

*Bechem, C.; Combs, J.; Utamaphethai, N.; Black, B.; Blanton, R.D.S.; Shen, J.P.;*  
Micro, IEEE, Volume: 19 Issue: 3, May-June 1999  
Page(s): 26 -35

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**7 Performance analysis and validation of the picoJava processor**

*Hangal, S.; O'Connor, M.;*  
Micro, IEEE, Volume: 19 Issue: 3, May-June 1999  
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---

**8 An approach for quantitative analysis of application-specific dataflow architectures**

*Kienhuis, B.; Deprettere, E.; Vissers, K.; Van Der Wolf, P.;*  
Application-Specific Systems, Architectures and Processors, 1997. Proceedings., IEEE International Conference on, 14-16 July 1997  
Page(s): 338 -349

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## 1 Exploring the cache design space for web servers

Iyer, R.;

Parallel and Distributed Processing Symposium., Proceedings 15th International , 23-27 April 2001

Page(s): 8 pp.

[\[Abstract\]](#) [\[PDF Full-Text \(456 KB\)\]](#) **IEEE CNF**

## 2 A method for design and performance modeling of client/server systems

Menasce, D.A.; Gomaa, H.;

Software Engineering, IEEE Transactions on , Volume: 26 Issue: 11 , Nov. 2000

Page(s): 1066 -1085

[\[Abstract\]](#) [\[PDF Full-Text \(520 KB\)\]](#) **IEEE JNL**

## 3 Performance analysis and validation of the picoJava processor

Hangal, S.; O'Connor, M.;

Micro, IEEE , Volume: 19 Issue: 3 , May-June 1999

Page(s): 66 -72

[\[Abstract\]](#) [\[PDF Full-Text \(1024 KB\)\]](#) **IEEE JNL**

## 4 Supercompilers for massively parallel architectures

Chapman, B.; Pantano, M.; Zima, H.;

Parallel Algorithms/Architecture Synthesis, 1995. Proceedings. First Aizu International Symposium on , 15-17 March 1995

Page(s): 315 -322

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Micro, IEEE, Volume: 19 Issue: 3, May-June 1999  
Page(s): 66 -72

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2 **Environment for PowerPC microarchitecture exploration**

*Moudgil, M.; Wellman, J.-D.; Moreno, J.H.;*  
Micro, IEEE, Volume: 19 Issue: 3, May-June 1999  
Page(s): 15 -25

[\[Abstract\]](#) [\[PDF Full-Text \(1948 KB\)\]](#) **IEEE JNL**

3 **Performance simulation of an Alpha microprocessor**

*Reilly, M.; Edmondson, J.;*  
Computer, Volume: 31 Issue: 5, May 1998  
Page(s): 50 -58

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4 **Calibration of microprocessor performance models**

*Black, B.; Shen, J.P.;*  
Computer, Volume: 31 Issue: 5, May 1998  
Page(s): 59 -65

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5 **PAT: state machine based approach to performance modeling for PowerPC™ microprocessors**

*Kumar, A.; Waldecker, B.;*  
Performance, Computing, and Communications Conference, 1997. IPCCC 1997., IEEE International, 5-7 Feb. 1997  
Page(s): 277 -283

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**6 Advanced alterable pipeline timer (adapt): a tool to design a high performance PowerPC™ microprocessor**

*Burgess, B.G.; Plummer Litch, S.;*

Performance, Computing, and Communications Conference, 1997. IPCCC 1997., IEEE International , 5-7 Feb. 1997

Page(s): 271 -276

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**7 Uninterpreted co-simulation for performance evaluation of Hw/Sw systems**

*Calvez, J.P.; Heller, D.; Pasquier, O.;*

Hardware/Software Co-Design, 1996. (Codes/CASHE '96), Proceedings., Fourth International Workshop on , 18-20 March 1996

Page(s): 132 -139

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**8 Optimal on-chip cache hierarchy synthesis with scaling of technology**

*Fu, S.T.; Flynn, M.J.;*

Computers and Communications, 1996., Conference Proceedings of the 1996 IEEE Fifteenth Annual International Phoenix Conference on , 27-29 March 1996

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**9 Performance modeling of the interconnect structure of a three-dimensional integrated RISC processor/cache system**

*Kuhn, S.A.; Kleiner, M.B.; Ramm, P.; Weber, W.;*

Components, Packaging, and Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on [see also Components, Hybrids, and Manufacturing Technology, IEEE Transactions on] , Volume: 19 Issue: 4 , Nov. 1996

Page(s): 719 -727

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**10 Timing analysis for synthesis of hardware interface controllers using timed signal transition graphs**

*Escalante, M.A.; Dimopoulos, N.J.; Gyuroff, D.; Muller, H.;*

Petri Nets and Performance Models, 1995., Proceedings of the Sixth International Workshop on , 3-6 Oct. 1995

Page(s): 232 -240

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**11 Performance modeling of the interconnect structure of a 3-dimensionally integrated RISC-processor/cache-system**

*Kuhn, S.A.; Kleiner, M.B.; Weber, W.;*

Electronic Components and Technology Conference, 1995. Proceedings., 45th , 21-24 May 1995

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**12 The PowerPC 603 microprocessor: performance analysis and design trade-offs**

*Poursepanj, A.; Ogden, D.; Burgess, B.; Gary, S.; Dietz, C.; Lee, D.; Surya, S.; Peters, M.;*  
Compcon Spring '94, Digest of Papers. , 28 Feb.-4 March 1994  
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**13 A microarchitectural performance evaluation of a 3.2 Gbyte/s microprocessor bus**  
*Stanley, T.; Upton, M.; Sherhart, P.; Mudge, T.; Brown, R.;*  
Microarchitecture, 1993. Proceedings of the 26th Annual International Symposium on , 1-3 Dec. 1993  
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*Hocevar, D.E.; Ching-Yu Hung; Pickens, D.; Sriram, S.;*  
*VLSI, 1998. Proceedings of the 8th Great Lakes Symposium on*, 19-21 Feb. 1998  
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*MacDonald, R.; Srinivasan, S.; Williams, R.; Aylor, J.;*  
*Rapid System Prototyping, 1992. 'Shortening the Path from Specification to Prototype', 1992 International*  
*Workshop on*, 23-25 June 1992  
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